

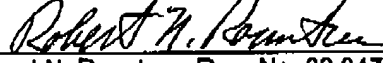
To: **Technology Center**
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From: **Robert N. Rountree**
Robert N. Rountree, LLC
Facsimile: 719-783-0990
Phone: 719-783-0990

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re the Application of: **Wang et al.**Docket Number: **TI-35376**Serial No.: **10/649,221**Art Unit: **2816**Filed: **08/25/2003**Examiner: **J.S. Zweizig**For: **TEMPERATURE INDEPENDENT CMOS REFERENCE VOLTAGE CIRCUIT FOR LOW-VOLTAGE APPLICATIONS****CERTIFICATION OF FACSIMILE TRANSMISSION**

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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Wang et al.	RECEIPT DATE & SERIAL NO.: 10/649,221
TITLE OF INVENTION: TEMPERATURE INDEPENDENT CMOS REFERENCE VOLTAGE CIRCUIT FOR LOW- VOLTAGE APPLICATIONS	FILING DATE: August 25, 2003
TI FILE NO.: TI-35376 DEPOSIT ACCT. NO.: 20-0668	
DATE FAXED: February 4, 2005	
DUE: December 17, 2004	
ATTY/SECY: Robert N. Rountree	

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Wang et al.**

Attorney Docket No.: **TI-35376**

Serial No.: **10/649,221**

Examiner: **J.S. Zweizig**

Filed: **August 25, 2003**

Art Unit: **2816**

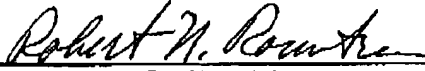
For: **TEMPERATURE INDEPENDENT CMOS REFERENCE VOLTAGE CIRCUIT FOR
LOW-VOLTAGE APPLICATIONS**

AMENDMENT UNDER 37 C.F.R. § 1.111

February 4, 2005

Commissioner for Patents
P.O. Box 1450
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Robert N. Rountree, Reg. No. 39,347

Dear Sir:

Reconsideration is requested in response to Examiner's rejection dated September
17, 2004. Please amend the instant application as follows.

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